

TERMINATE LINE

RIBBON CABLE ASSY TO RADIO

SET BULK DELAY

- A ZERO
- B 256 BITS
- C 512 BITS

DATA INVERT

SET VARIABLE DELAY
0 - 256 BITS

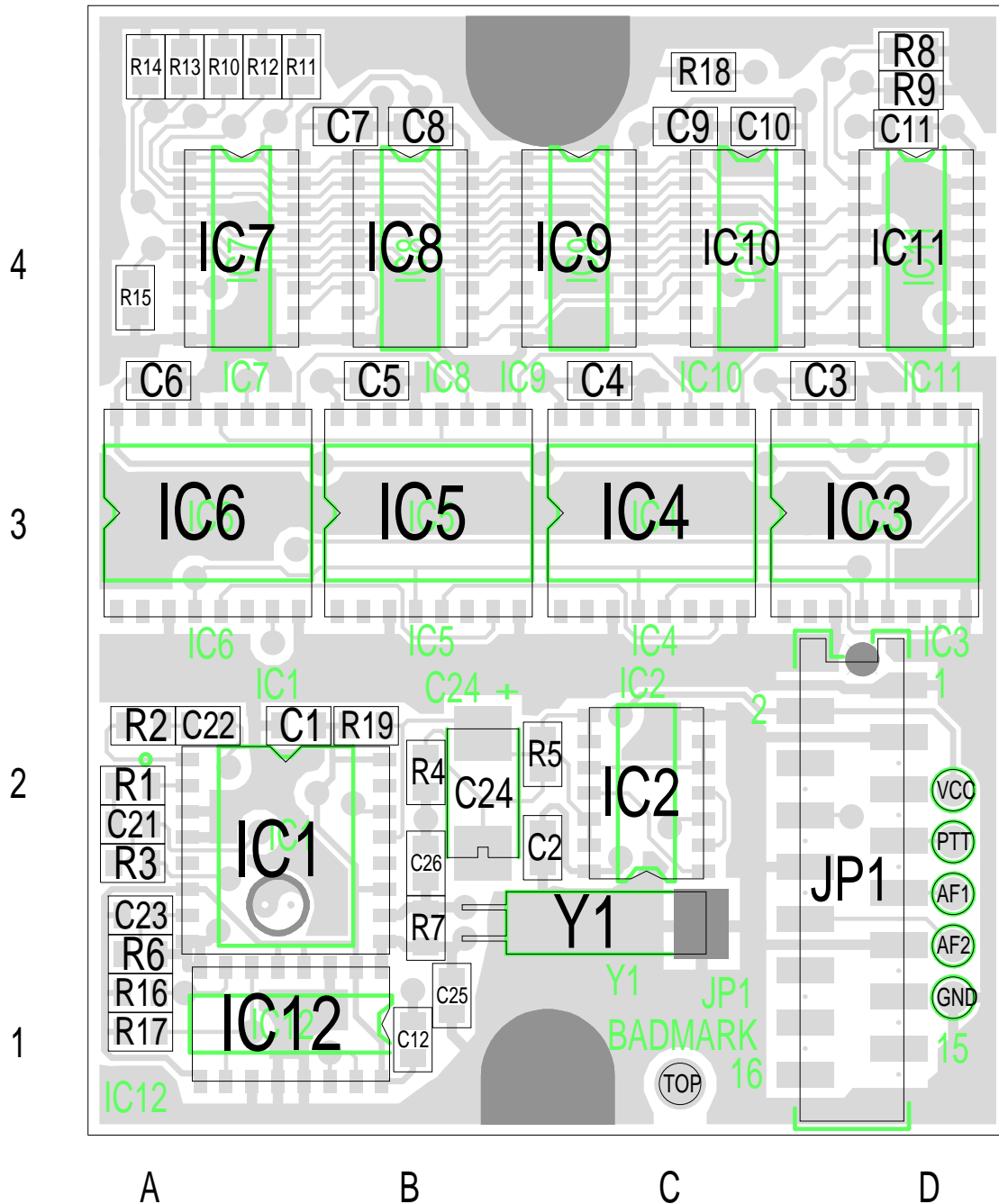
2-3 300kHz CLOCK / 3.35 μ S PER BIT (1km APPROX)
1-2 150kHz CLOCK / 6.7 μ S PER BIT (2km APPROX)

02A	UPDATE FOR PRODUCTION	PAUL G			03/9/99
01A	ED. INCORRECT FOOTPRINT	PAUL G			29/7/99
00A	CREATED	PAUL G			6/7/99
REV/ISS	AMENDMENTS	DRAWN	CHKD	D.O.	APVD DATE

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T800-37
MODEM/DELAY UNIT

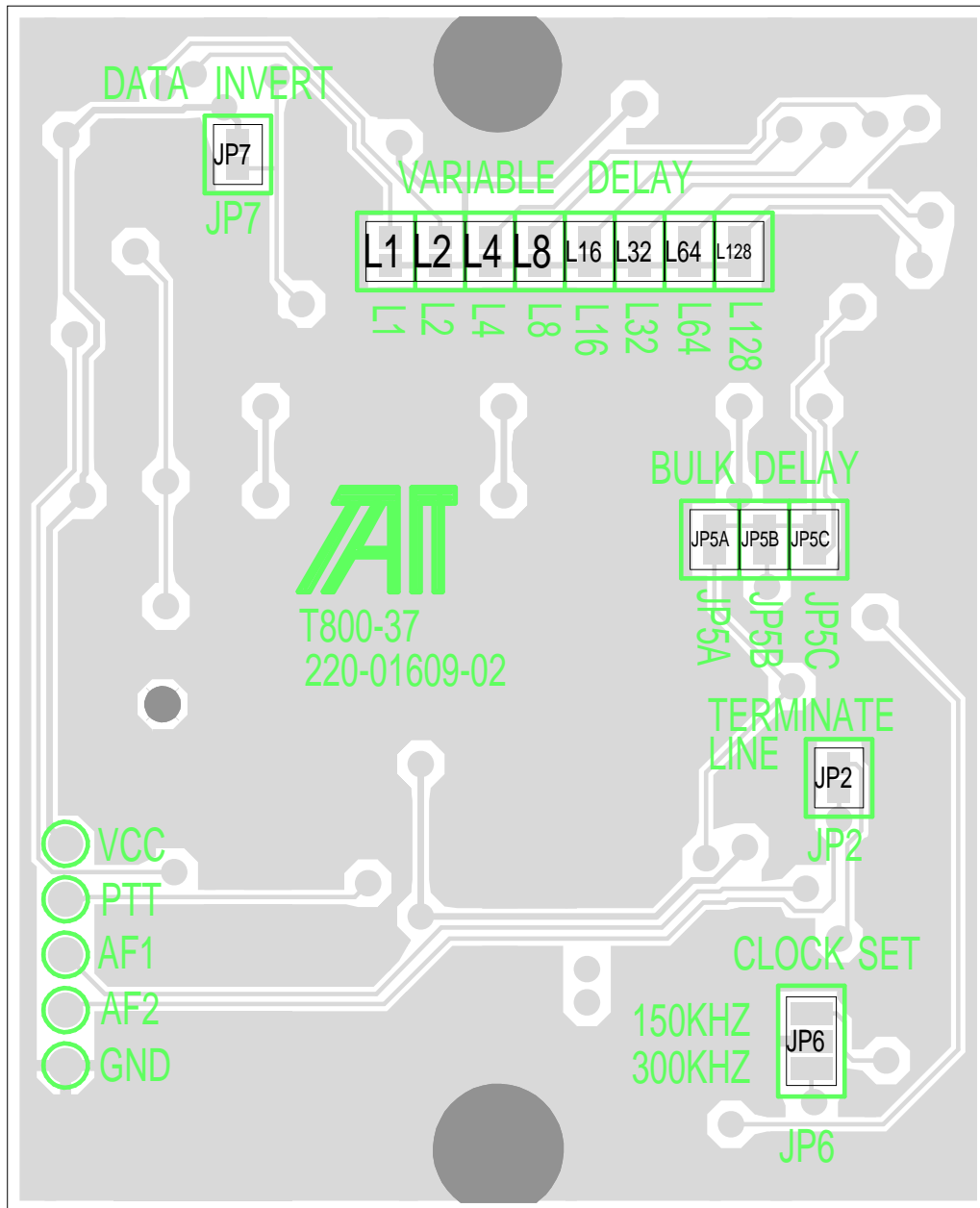
IPN: 220-01609-02 ISSUE: A ID: 2.S.C. 1

PROJECT: T800 DESIGNER: D BALLOCH FILE NAME: 160902a FILE DATE: 6-Sep-99 NO.SHEETS: 1



TAIT ELECTRONICS	IPN:	ISS:	ID:	DATE:
	220-01609-02	A	1.TA	6 Sep 1999
T800-37-0000 PCB LAYOUT - TOP SIDE				

Scale: 3:1 ; Rotation: 0 degrees



D

C

B

A

TAIT ELECTRONICS	IPN:	ISS:	ID:	DATE:
	220-01609-02	A	2.BA	6 Sep 1999
T800-37-0000 PCB LAYOUT - BOTTOM SIDE				